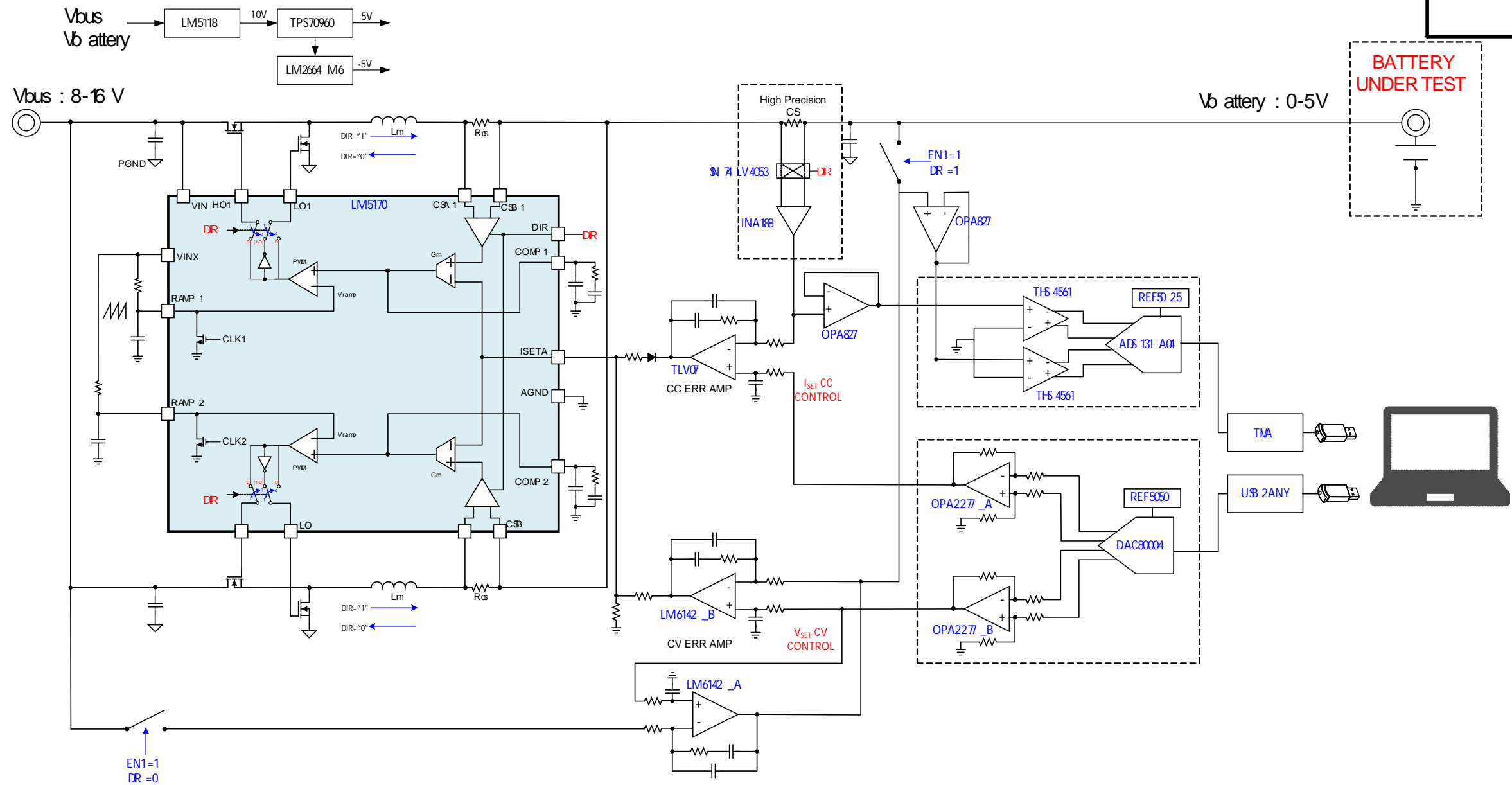
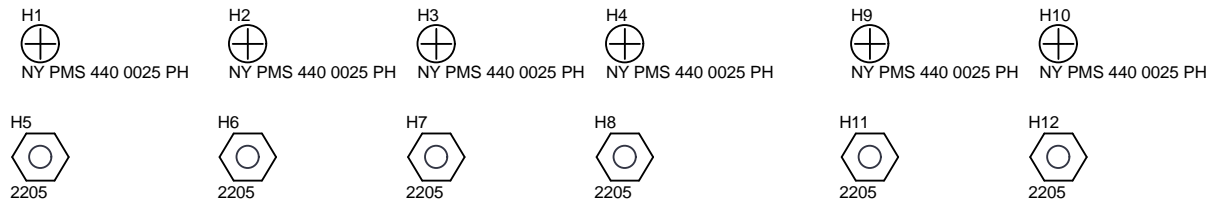


Revision History	
Revision	Notes



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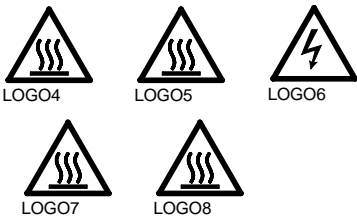
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TID #: TIDA-01042	Project Title: Battery Tester Reference Design for High Current Applications	
Number: TIDA-01042	Rev: A	Sheet Title: Block Diagram
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 11
Drawn By:	File: SV601272A-Cover.SchDoc	Size: B
Engineer: Michael Balourdas, Taras Dotsenko	Contact: http://www.ti.com/support	



PCB Number: TIDA-01042
PCB Rev: A

Logo2
PCB
LOGO
Pb-Free Symbol

PCB
LOGO
FCC disclaimer



LBL1

PCB Label

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

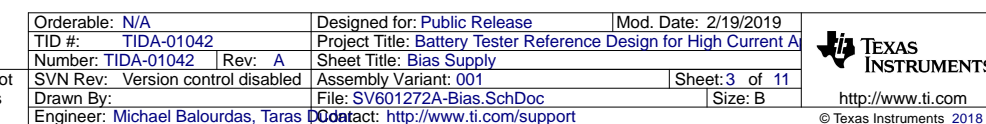
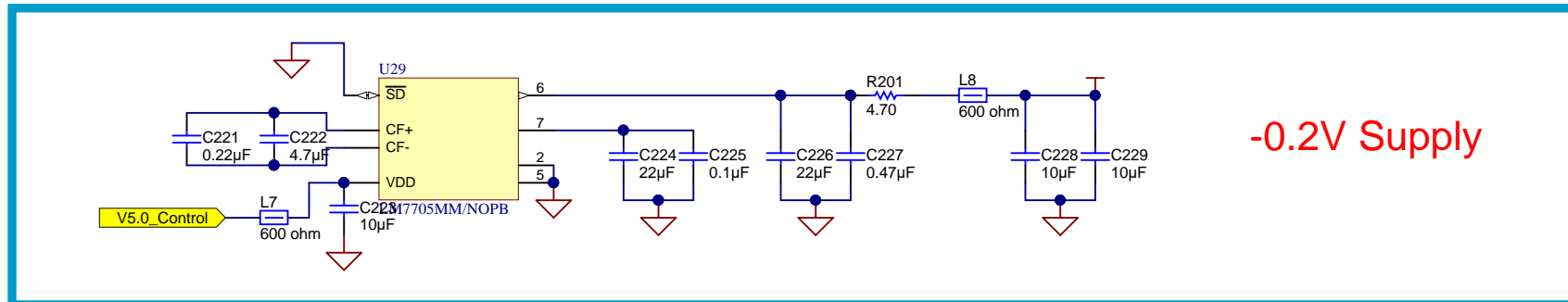
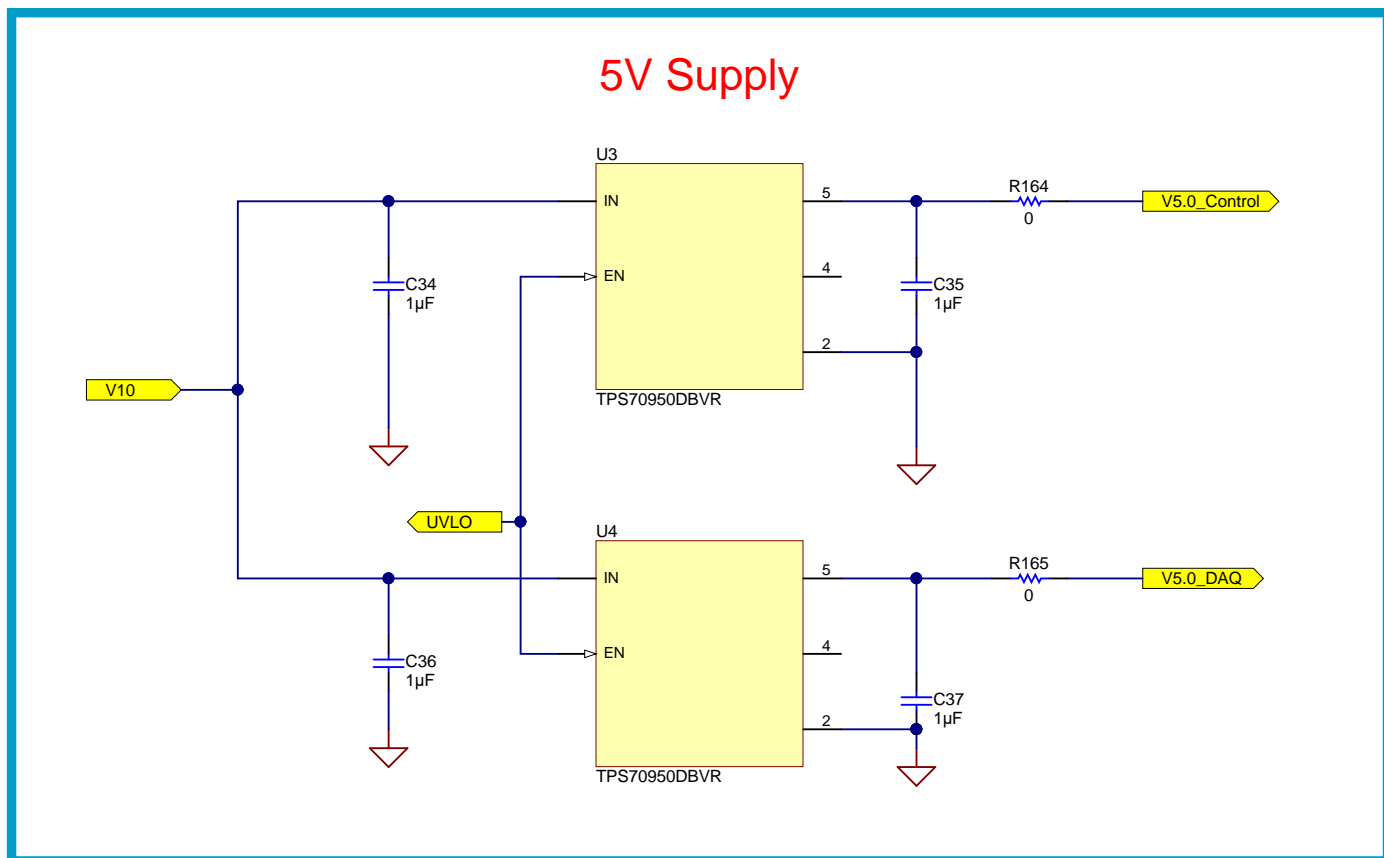
Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

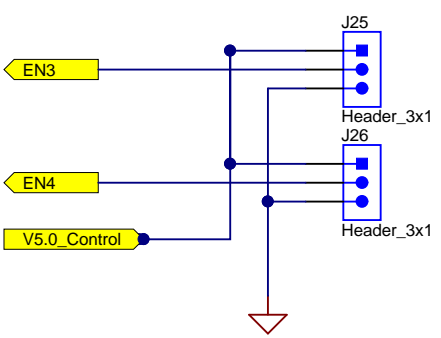
Label Table	
Variant	Label Text
001	
002	

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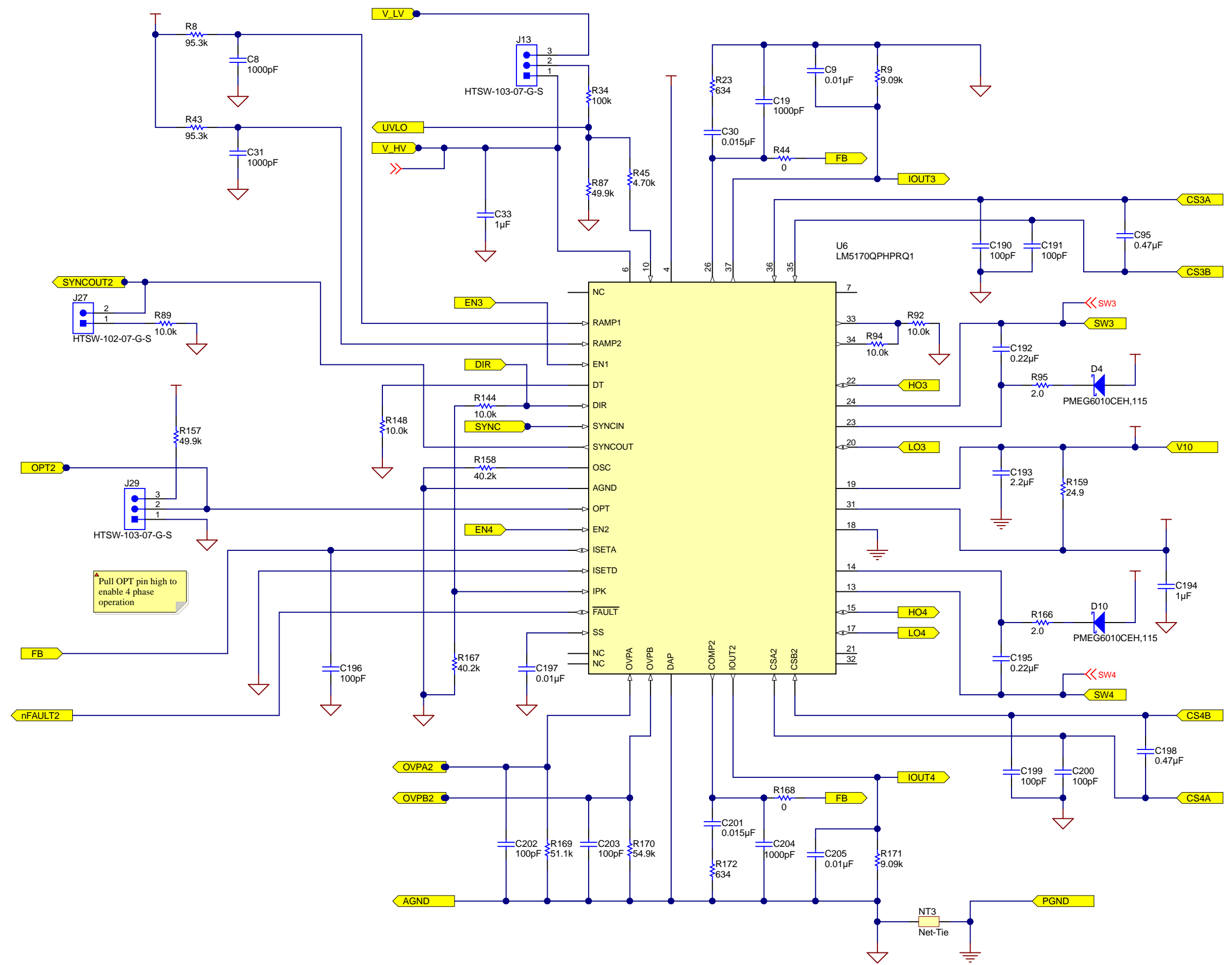
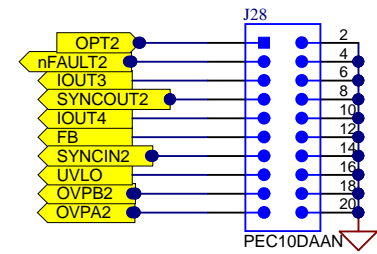
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Number: TIDA-01042	Rev: A	Sheet Title: Hardware
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 11
Drawn By:	File: SV601272A-HW.SchDoc	Size: B
Engineer: Michael Balourdas, Taras D	Contact: http://www.ti.com/support	



Enable Jumpers

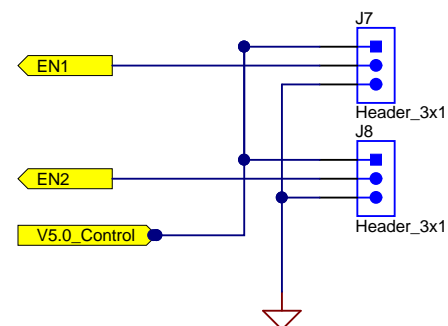


Optional Ground Jumpers

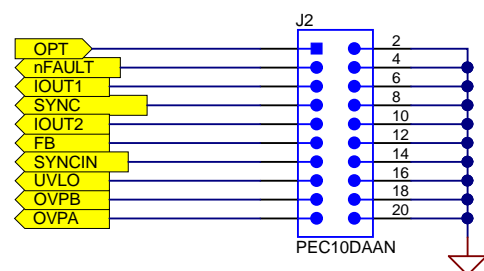


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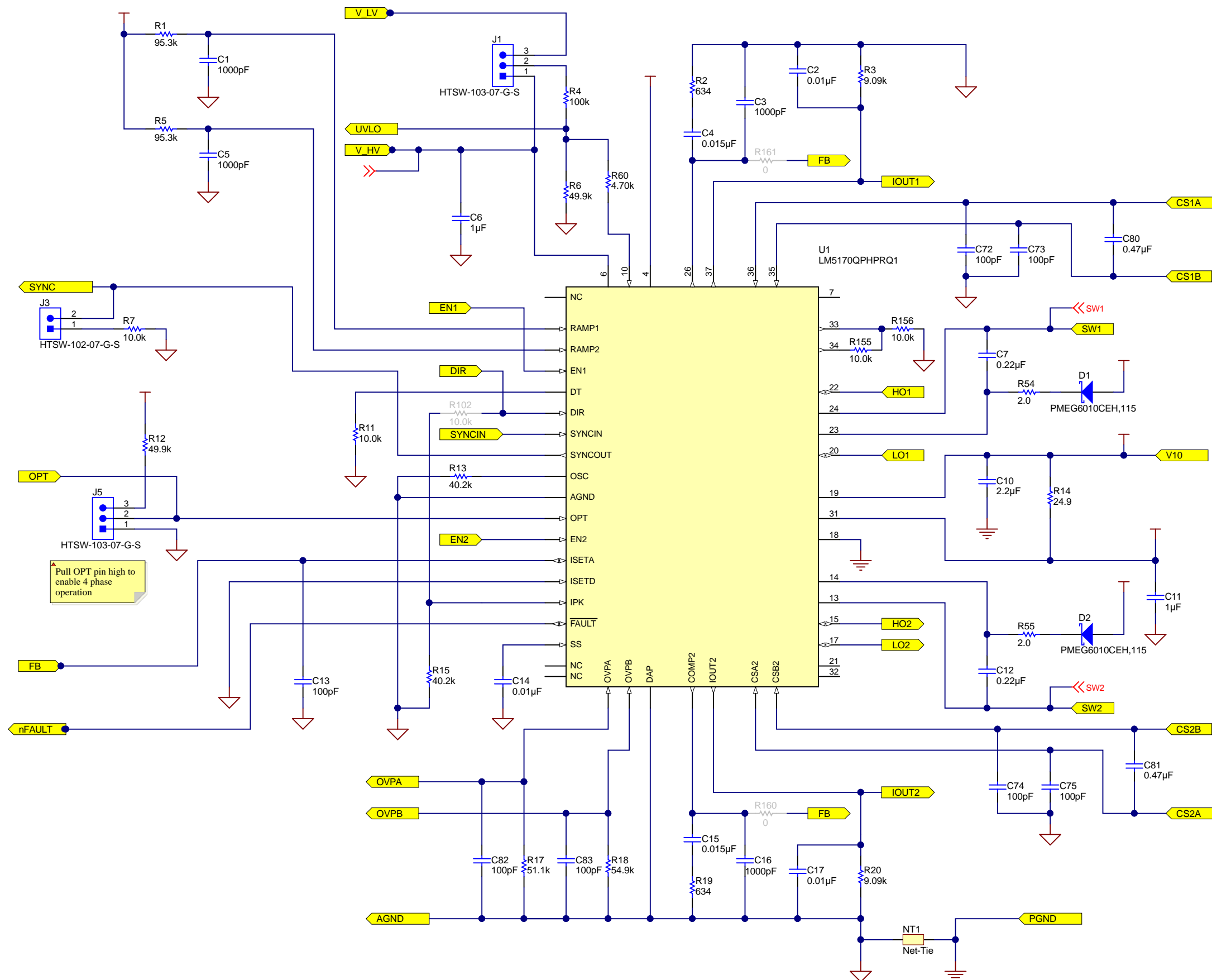
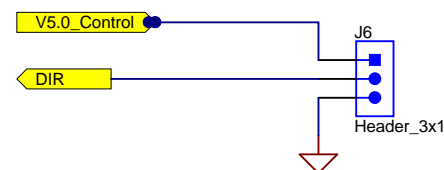
Enable Jumpers

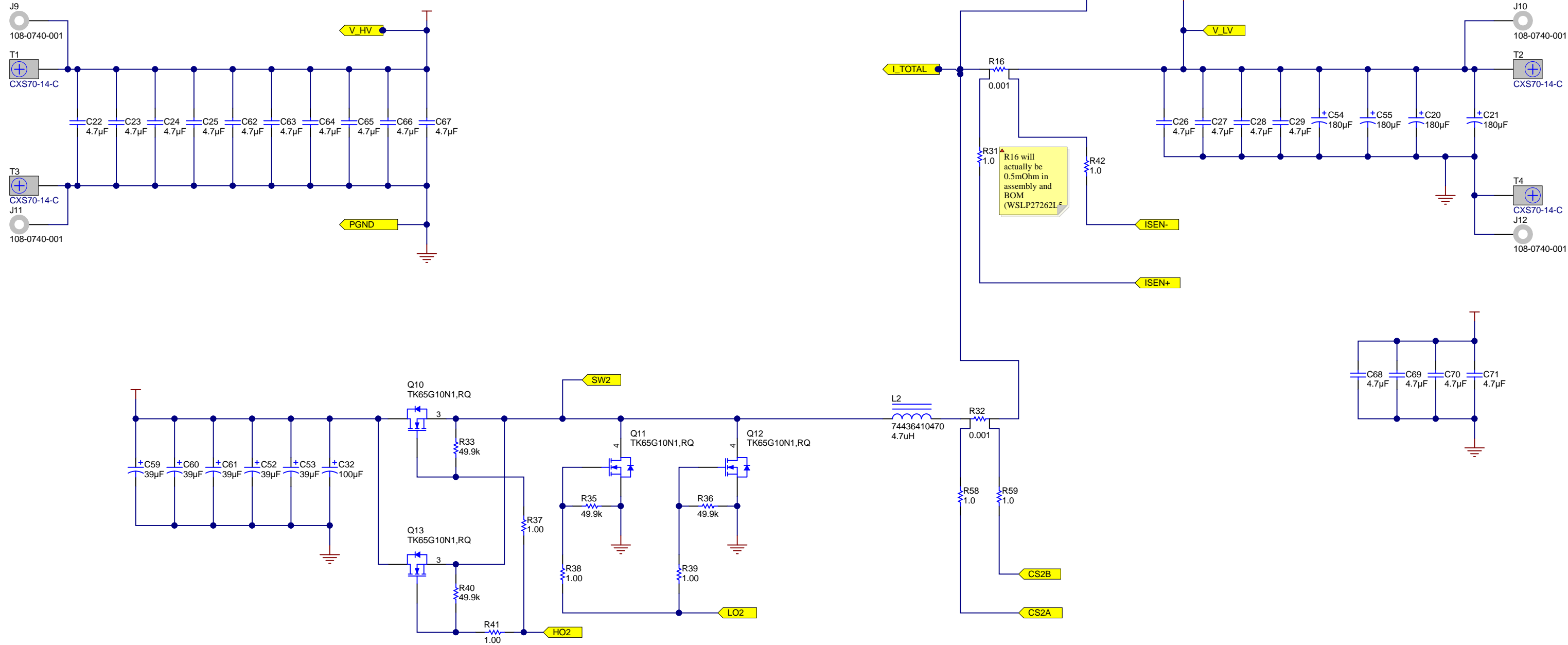


Optional Ground Jumpers

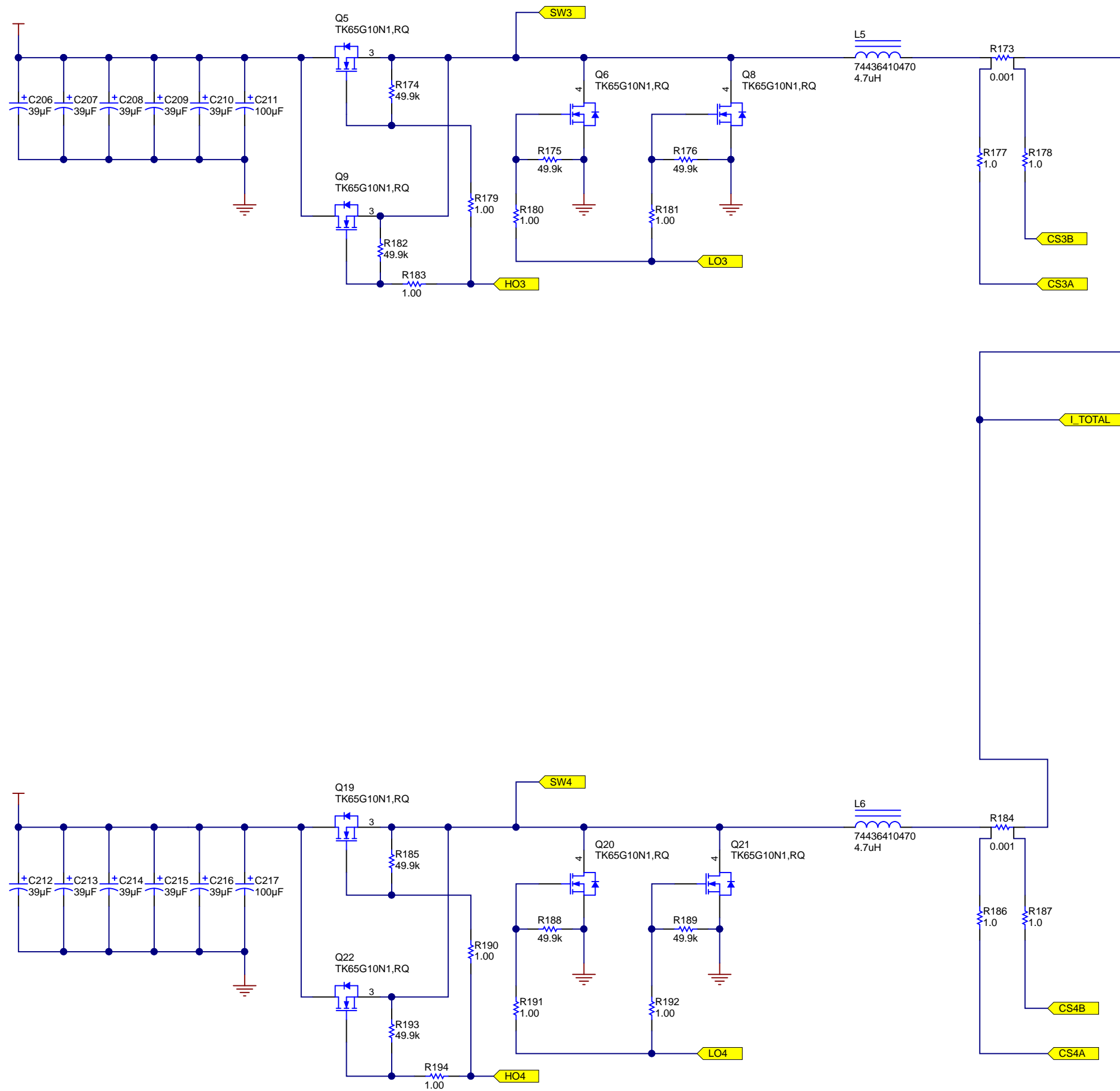


Direction Control

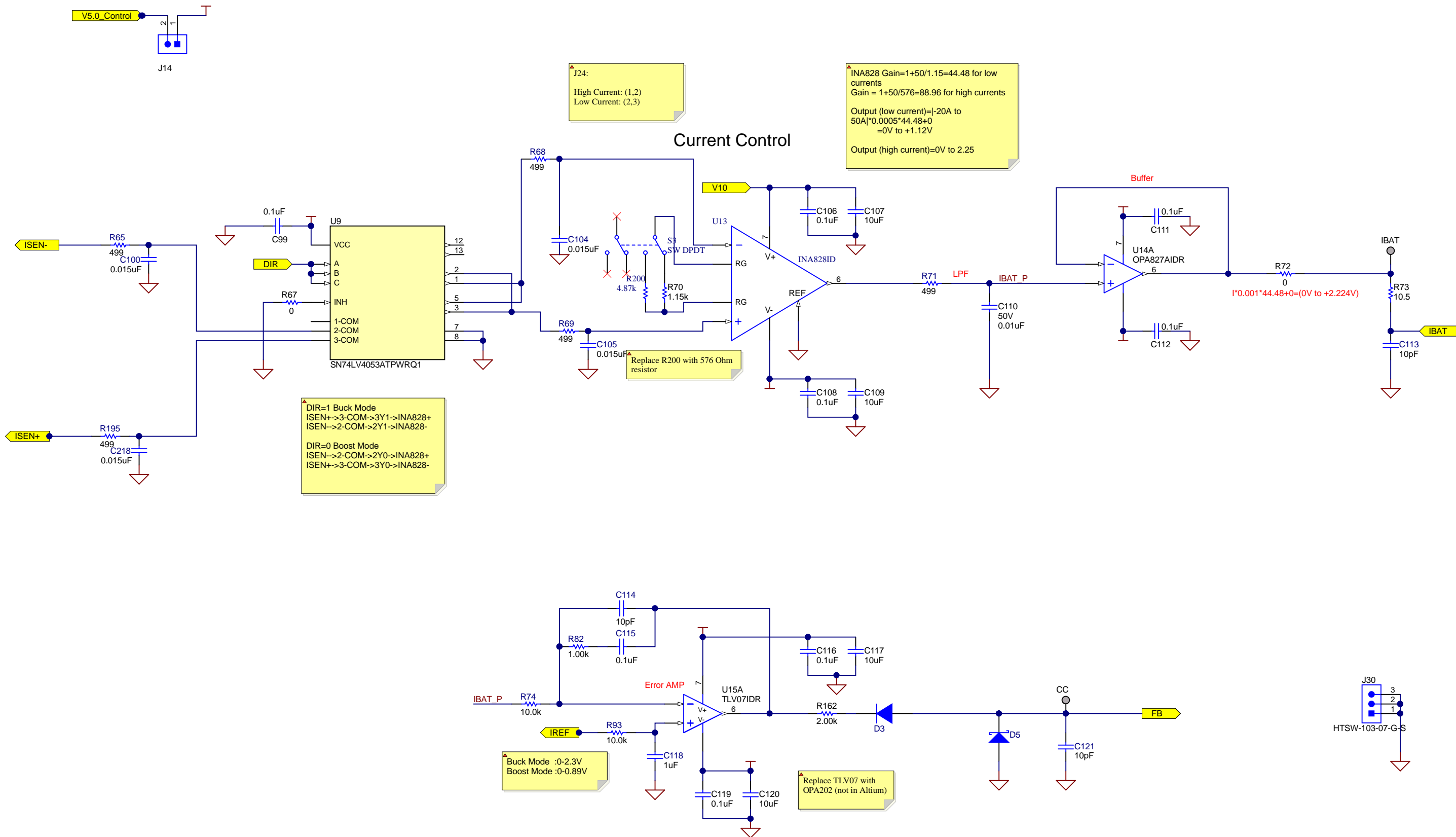




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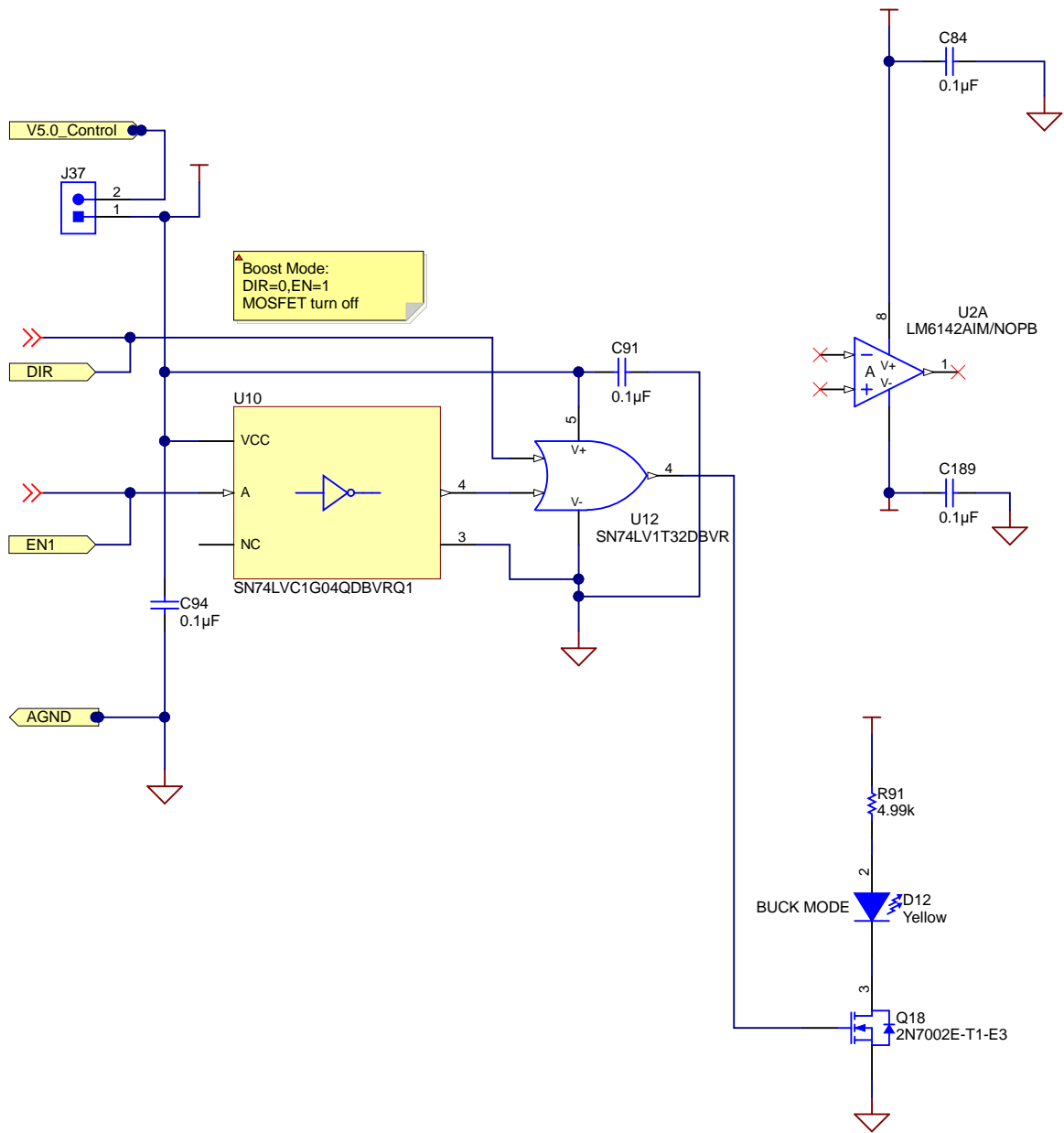


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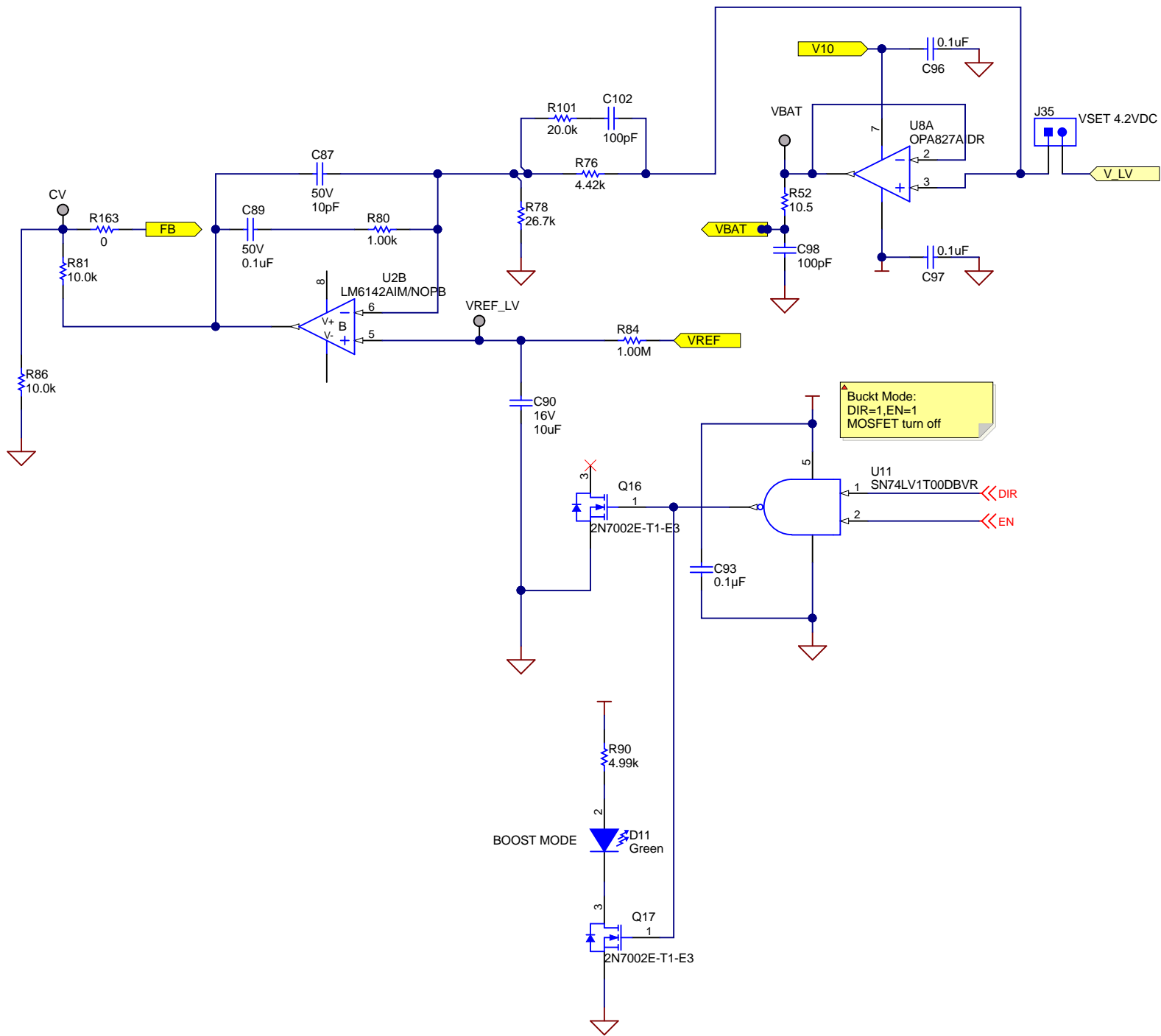
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TID #: TIDA-01042	Project Title: Battery Tester Reference Design for High Current A	
Number: TIDA-01042	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 6 of 11
Drawn By:	File: SV601272A-Current-Control REV3.SchDoc	Size: B
Engineer: Michael Balourdas, Taras D	Contact: http://www.ti.com/support	

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BOOST CONTROL



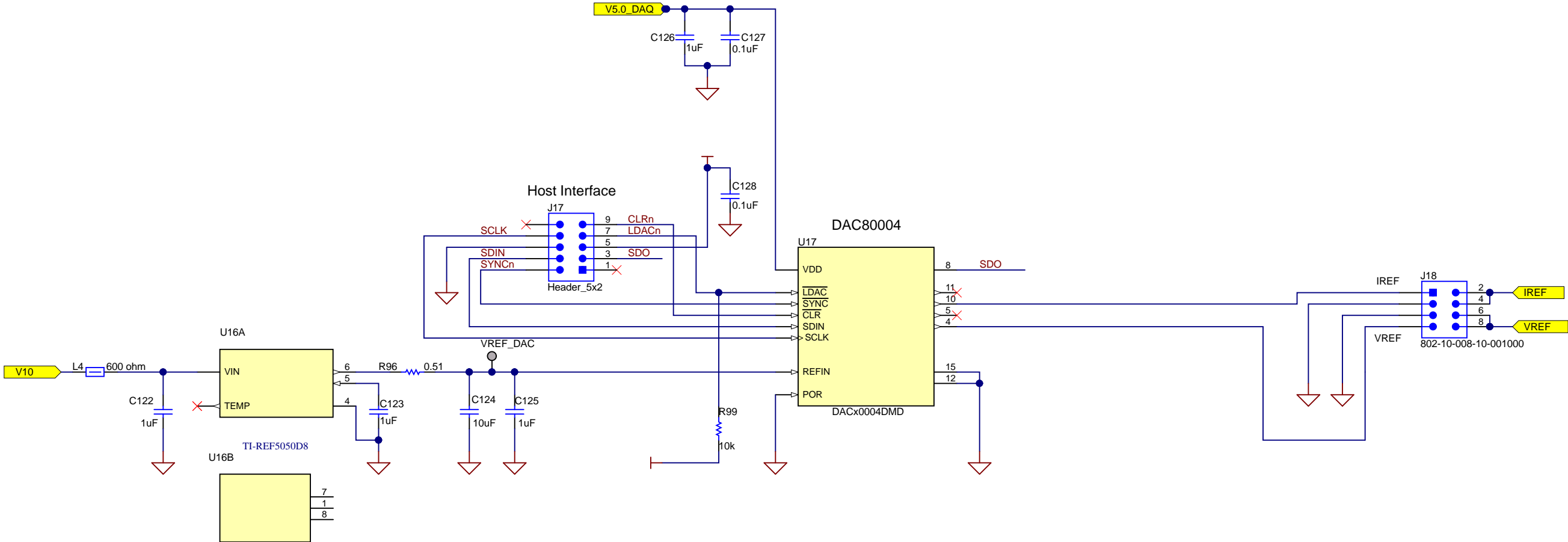
BUCK CONTROL

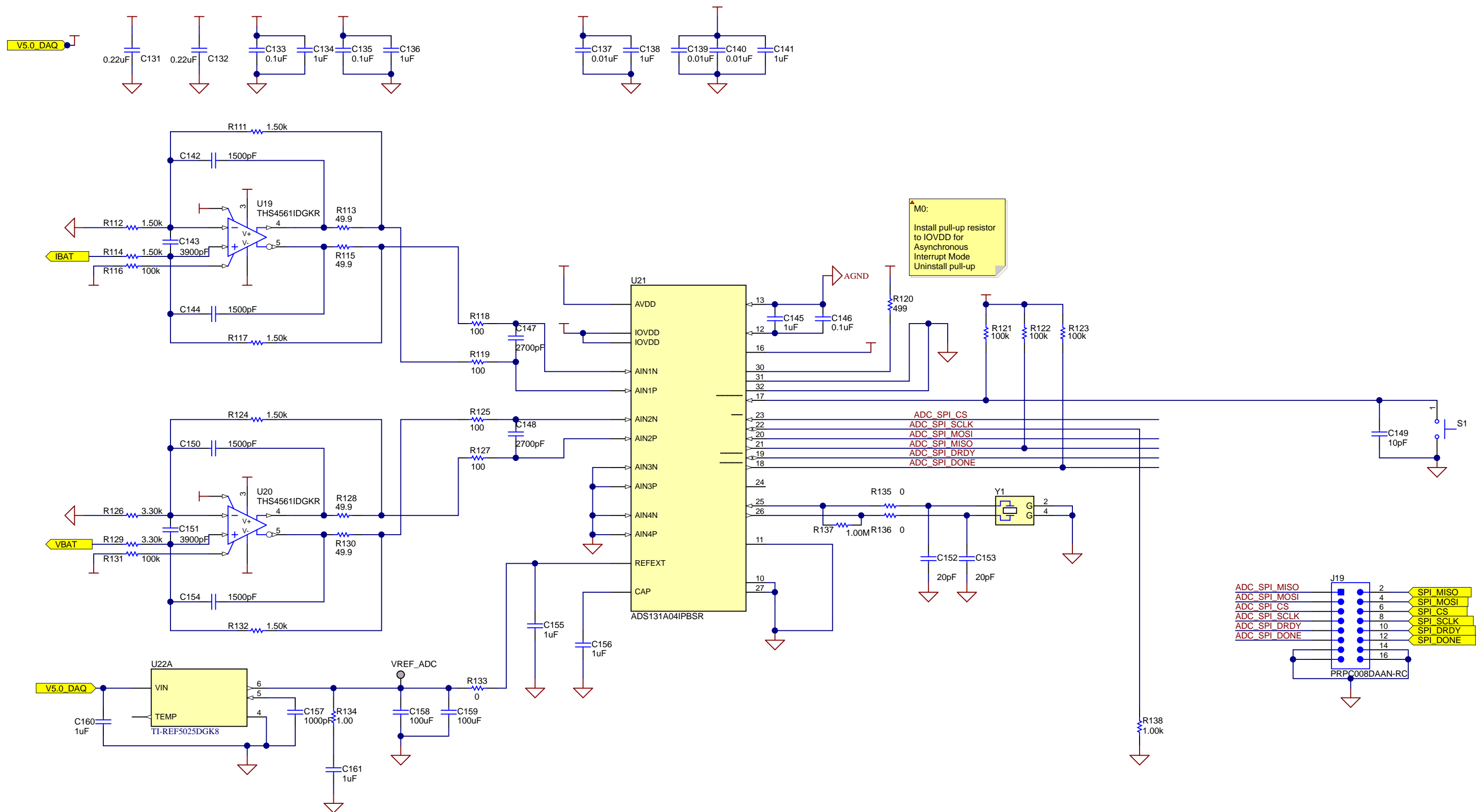


DAC

IREF
Output Range:0V-2.3V

VREF
Output Range:0V-4.1V

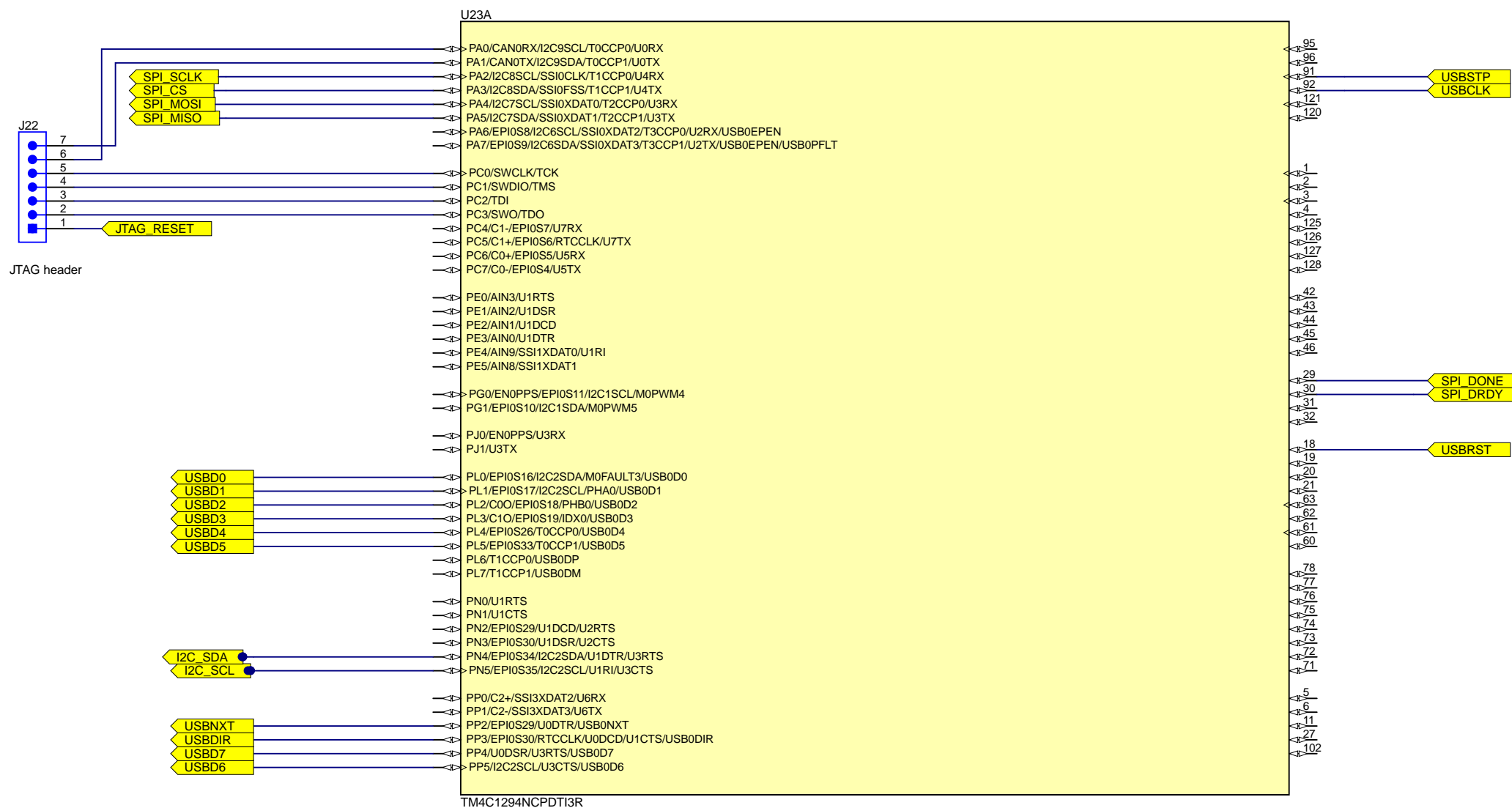
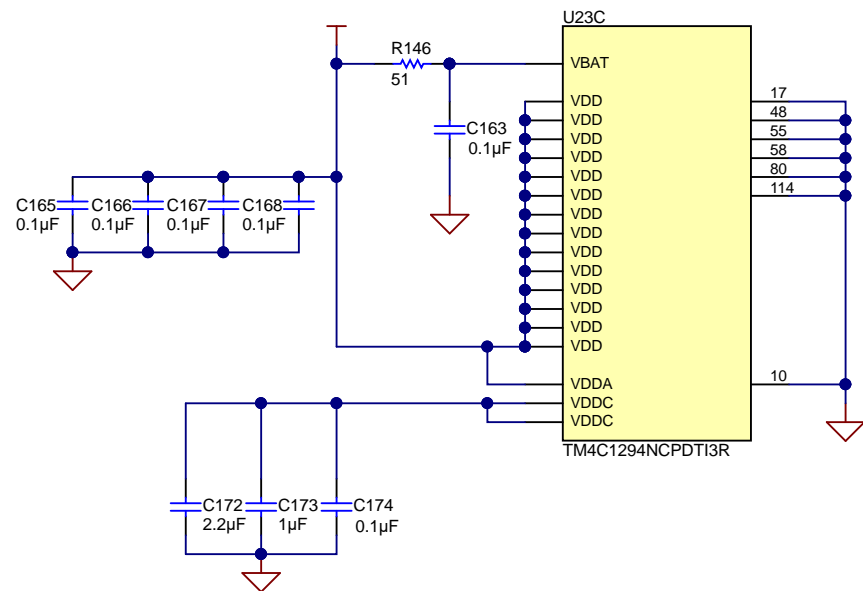
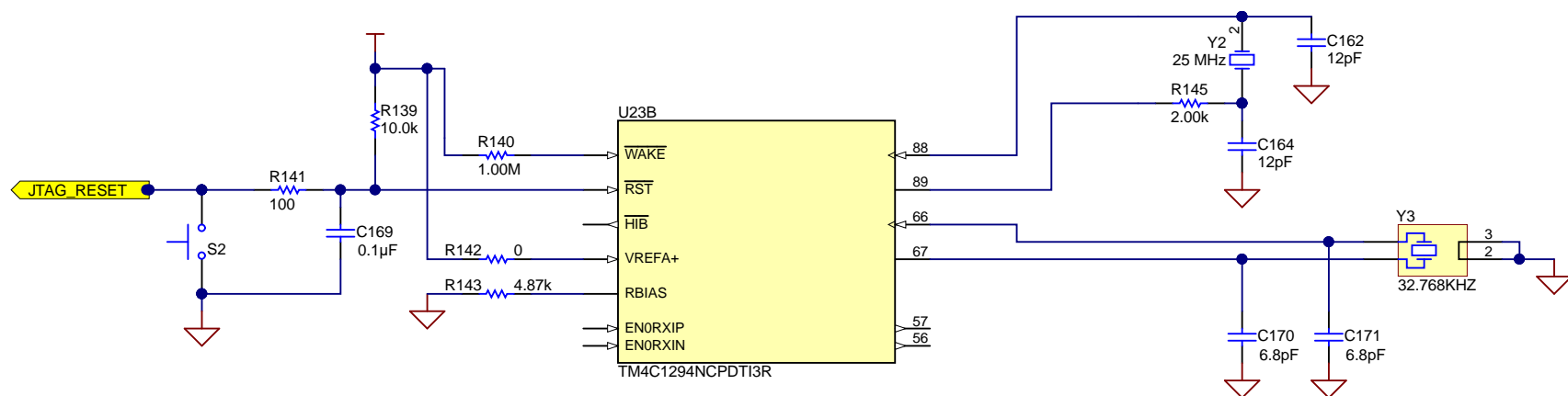




M0:
Install pull-up resistor
to IOVDD for
Asynchronous
Interrupt Mode
Uninstall pull-up

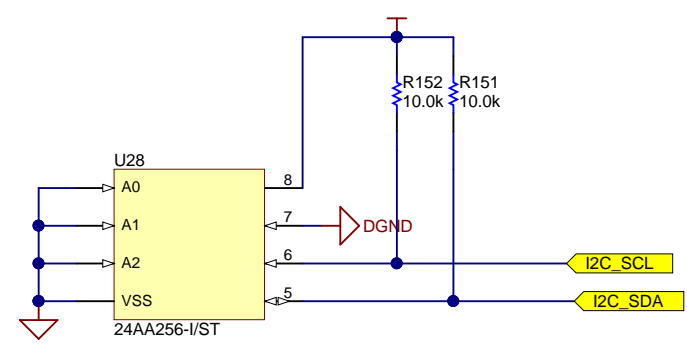
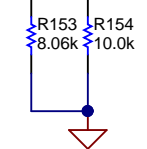
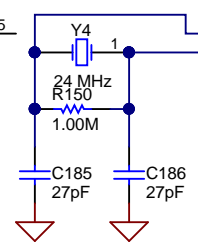
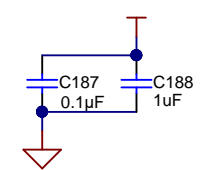
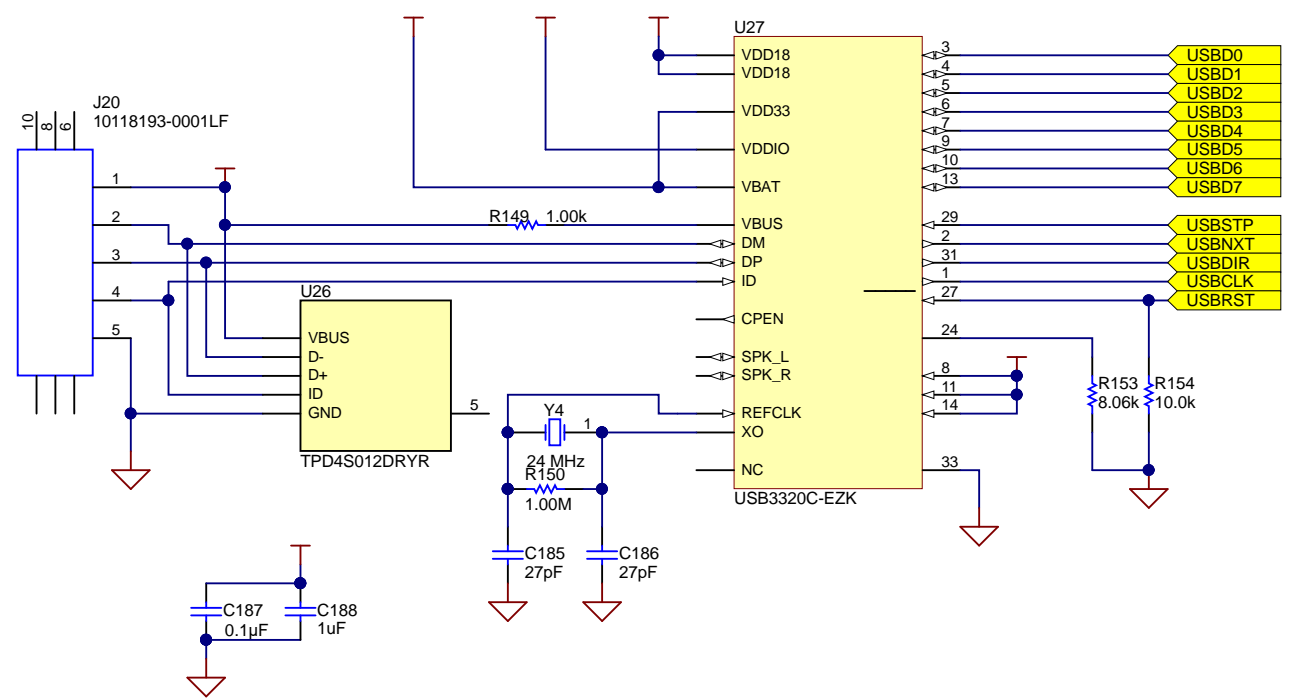
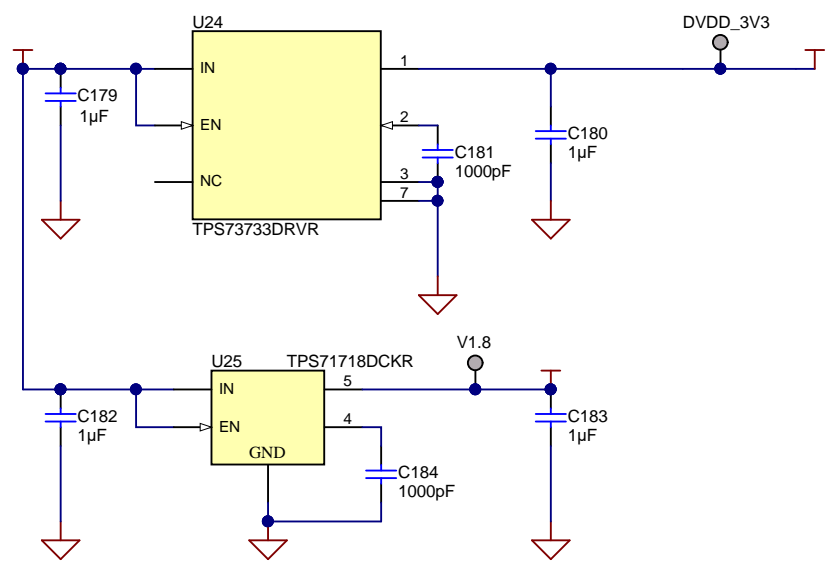
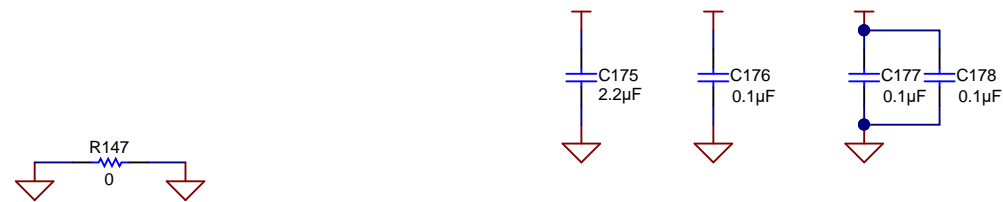
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Orderable: N/A	Designed for: Public Release	Mod. Date: 2/19/2019
TID #: TIDA-01042	Project Title: Battery Tester Reference Design for High Current A	
Number: TIDA-01042	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 9 of 11
Drawn By:	File: SV601272A-ADC.SchDoc	Size: B
Engineer: Michael Balourd, Taras D	Contact: http://www.ti.com/support	



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Orderable: N/A	Designed for: Public Release	Mod. Date: 2/19/2019
TID #: TIDA-01042	Project Title: Battery Tester Reference Design for High Current A	
Number: TIDA-01042	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 10 of 11
Drawn By:	File: SV601272A-TM4C_Main.SchDoc	Size: B
Engineer: Michael Balourdass, Taras D	Docuact: http://www.ti.com/support	



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